

Docket No. 50265-0018

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

David C. Chapman

Serial No. 09/421,437

Filed: October 19, 1999



) Group Art Unit: 2768

) Examiner: A. Thompson

For: APPROACH FOR ROUTING AN INTEGRATED CIRCUIT

Box Amendment  
Commissioner for Patents  
Washington, D.C. 20231

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Technology Center 2100

Dear Sir:

Transmitted herewith is an Amendment in the above identified application.

- ☐ No additional fee is required.
- ☒ Also attached: Return Receipt Postcard.

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	34	33	1	\$9.00 =	\$9.00
Independent Claims	6	6	0	\$40.00 =	\$0.00
Multiple claims newly presented					\$0.00
Fee for extension of time					\$0.00
TOTAL FEE DUE					\$9.00

- ☒ Enclosed is a check in the amount of \$9.00. Please charge any and all deficiencies to Deposit Account No. 50-1302. An additional copy of this transmittal sheet is submitted herewith.
- ☒ The Commissioner is hereby authorized to charge payment of any fees associated with

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this communication or credit any overpayment, to Deposit Account No. 50-1302, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

HICKMAN PALERMO TRUONG & BECKER LLP



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**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Box Amendment, Commissioner for Patents, Washington, DC 20231

on November 15, 2000

by



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JEL  
12.6.00

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AMENDMENT AND RESPONSE

Sir:

In response to the Office Action mailed August 16, 2000, please amend the application referenced above as indicated hereinafter.

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IN THE CLAIMS:

Please add new Claim 34 and amend the claims as indicated hereinafter.

Although not all of the claims have been amended, all claims are reproduced for convenience.

- 1 1. (AMENDED) A method for automatically routing an integrated circuit, the method  
2 comprising the computer-implemented steps of:  
3 receiving integrated circuit layout data that defines a set of two or more integrated  
4 circuit devices to be included in the integrated circuit;

Barndtson  
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